

## PATENT ABSTRACTS OF JAPAN

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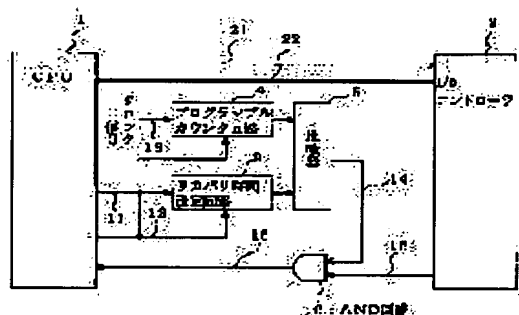
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## (54) I/O ACCESS CONTROL CIRCUIT

## (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide an I/O access control circuit with which a conventional program can be diverted even in the case of a change to a CPU at accelerated processing speed.

**SOLUTION:** When recovery time has been set to a recovery time setting circuit 3 by a CPU 1, a clock 13 is counted by a programmable counter circuit 4, a comparator 5 compares the set recovery time with the added value of clocks 13. When the added value reaches the recovery time, a coincidence signal is outputted and when a response signal 15 from an I/O controller 2 and the coincidence signal 14 are inputted corresponding to the access of the CPU 1, an AND circuit 6 outputs a signal 16 for setting the recovery time to the CPU 1.



## LEGAL STATUS

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